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HiddenExpeditionAmazoncrackeddownload ITEM DESCRIPTION Inventory 2.0.5.2 APK + Mod DATA File .1. Field of the Invention The present invention relates to a method of manufacturing a semiconductor device, in particular to a method of forming a pattern by exposing and developing, using resist patterns, a photosensitive film on a substrate, and a method of manufacturing a semiconductor device having a pattern. 2. Description of the Related Art A DRAM includes a capacitor. In the capacitor of the DRAM, an oxide film is typically formed on a semiconductor substrate of a silicon (Si) type or a material similar thereto as a capacitor insulating film. Further, a polycrystalline silicon film or a silicon nitride film is typically formed as a capacitor electrode. A silicon nitride film is superior in a stress characteristic, a dielectric characteristic, an insulating property and the like. Therefore, a silicon nitride film is preferably used as a capacitor insulating film. A silicon nitride film is formed by a CVD process, a PVD process or the like. When a silicon nitride film is formed by the CVD process or the like, particles are likely to be contained in the formed silicon nitride film. Therefore, a process of reducing particles is required in a silicon nitride film formation step. In general, a silicon nitride film is formed through a deposition process followed by a heat treatment process. Therefore, it is believed that silicon nitride films are formed in a high temperature range. Further, the heat treatment process in the silicon nitride film formation step is performed in a batch system. In the batch system, a large number of substrates are subjected to the heat treatment process in a heater in the same amount of time. In recent years, a low-k film is formed on the entire surface of a semiconductor wafer on which a DRAM is formed or the entire surface of a semiconductor wafer, in which a DRAM is formed. The low-k film is formed on a pad portion (including an interlayer insulating film) exposed from a wiring layer, for example, to reduce the wiring delay of the DRAM. In general, a silicon nitride film is formed on the entire surface of a substrate to improve the film stress characteristic of the low-k film. However, when a silicon nitride film is formed on the entire surface of the substrate, a thinner silicon

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